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TITLE: Lower substrate structure for liquid crystal display

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APPLICATION-DATA:

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ABSTRACTED-PUB-NO: KR2001058176A

BASIC-ABSTRACT:

NOVELTY - A lower substrate structure for a liquid crystal display is to arrange a thin film transistor correspondingly to the rubbing direction of the orientation of the lower substrate to minimize a rubbing defect of the thin film transistor, thereby improving image quality of the liquid crystal display.

DETAILED DESCRIPTION - A gate bus line(21) is extended in a certain direction on a lower substrate(20). A data bus line(22) is extended substantially vertically to the gate bus line, so that a pixel unit is defined by the data bus line and the gate bus line. A thin film transistor(25) is disposed on the gate bus line that intersects the data bus line. A pixel electrode(27) is disposed in a space of the pixel unit while contacting a certain portion of the

thin film transistor. On the resultant object is disposed an oriented film rubbed in a certain direction. The thin film transistor is comprised of a gate electrode parallel to the rubbing direction of the oriented film, an active layer overlapped with the gate electrode, a source electrode in contact with one side of the active layer, and a drain electrode in contact with the other side of the active layer and the pixel electrode.

CHOSEN-DRAWING: Dwg.1/10

TITLE-TERMS: LOWER SUBSTRATE STRUCTURE LIQUID CRYSTAL DISPLAY

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